

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	0	9-218781	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 08:41
S2	7	"9218781"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 10:29
S3	22	"6034538"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 09:06
S4	8	(reconfigur\$4 hardware).ti. AND (command OR instruction) near2 (control OR manager)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 09:46
S5	29	("4541071" "4942319" "5493239" "5508636" "5572148" "5640106" "5654650" "5760607" "5773993" "5838165").PN. OR ("6034538").URPN.	US-PGPUB; USPAT; USOCR	ADJ	ON	2006/12/14 09:10
S6	1	(reconfigur\$4 hardware) AND control (device OR manager) NEAR10(stop OR halt OR activate OR load OR cancel OR wait) NEAR2 instruction	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 09:48
S7	286867	(reconfigurable hardware OR reconfigurable logic OR programmable logic array OR PLA OR programmable logic devices OR PLD OR Field Programmable Gate Array OR FPGA OR Programmable Array Logic OR PAL OR EPLD EEPLD OR Logic Cell Arrays OR LCA)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 09:54

S8	10580	S7 AND control device	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 09:50
S9	2035	S7 AND (control device SAME (switch\$3)) AND (circuit model OR netlist OR page OR circuit portion OR tiles OR blocks)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 09:52
S10	84	S7 AND (control device SAME (switch\$3)) SAME (circuit model OR netlist OR page OR circuit portion OR tiles OR blocks) SAME (activate OR halt OR interrupt OR load OR cancel OR wait)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 09:53
S11	44919	(reconfigurable hardware OR reconfigurable logic OR programmable logic array OR programmable logic devices OR Field Programmable Gate Array OR Programmable Array Logic OR Logic Cell Arrays)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 09:55
S12	1337908	(netlist OR page OR circuit portion OR tiles)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 09:55
S13	601	S11 AND (S12 NEAR10 load \$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 09:56
S14	470	S11 AND S12 AND (control NEAR5 address NEAR5 (instruction OR command))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 09:59
S15	116	S11 AND S12 AND (control NEAR5 address NEAR5 (instruction OR command)) AND program memory AND interrupt	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 10:05

S16	953	S11.ti. AND process\$3 AND memory	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 10:06
S17	45	S11.ti. AND process\$3 AND memory AND (control NEAR5 instruction)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 10:27
S18	3	"2001331767"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 10:27
S19	22	"1195994"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 10:28
S20	0	"11-95994"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 10:28
S21	2	"09218781"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 10:30
S22	7	"3075166"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 10:30
S23	44919	(reconfigurable hardware OR reconfigurable logic OR programmable logic array OR programmable logic devices OR Field Programmable Gate Array OR Programmable Array Logic OR Logic Cell Arrays)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 13:59
S24	41	S23.ti. AND (address (register OR counter OR stack) NEAR5 control)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 14:04

S25	2	S23.ti. AND page.clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 14:09
S26	11	S23.ti. AND independent NEAR2 control	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 14:11
S27	124	S23.ti. AND address signal	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 14:12
S28	15	S23 AND master parallel	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 15:34
S29	64	(S23 AND master serial) NOT S28	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 15:21
S30	4	"10319051"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 15:36
S31	9	"6907595"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 15:36
S32	399	S23 AND cache AND address translation	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 15:53
S33	0	S23 AND cache SAME address translation SAME \$2configuration data	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 15:53

S34	77	S23 AND cache and address translation and \$2configuration data	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 15:54
S35	0	S23.ti. AND cache and address translation	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 15:56
S36	0	S23.ti. AND cache AND address NEAR2 translation	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 15:57
S37	0	S23.ti. AND cach\$4 AND address NEAR2 translation	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 15:57
S38	64	S23.ti. AND cach\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 15:58
S39	0	S23.ti. AND cach\$4 NEAR3 manager AND cach\$4 NEAR3 controller	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 15:59
S40	0	S23.ti. AND cach\$4 NEAR3 manager	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 15:59
S41	16	S23.ti. AND cach\$4 NEAR3 control\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/15 11:01
S42	68	S23.ti. AND cach\$	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 16:09

S43	40	"5,600,263"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 16:09
S44	40	"5600263"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 16:09
S45	3	S23.ti. AND interrupt (vector OR command)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 16:32
S46	81	S23.ti. AND interrupt	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 16:32
S47	1	S23.ti. AND control flow analysis	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 16:44
S48	117	"5021947"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/14 16:59
S49	44919	(reconfigurable hardware OR reconfigurable logic OR programmable logic array OR programmable logic devices OR Field Programmable Gate Array OR Programmable Array Logic OR Logic Cell Arrays)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/15 08:13
S50	106	S49 AND (self reconfiguration OR self re\$1configuration OR selfconfiguration OR self configuration OR self \$1configuration)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 08:17
S51	28	("5684980" "5802290" "5838167" "6094063" "Re34363").PN. OR ("6326806").URPN.	US-PGPUB; USPAT; USOCR	ADJ	ON	2006/12/15 09:49

S52	39	"6091263"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/15 10:12
S53	5	"6091263" AND cache	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/15 10:13
S54	482	S49 AND cach\$4 control\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/15 11:01
S55	38	S49 SAME cache controller	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/15 11:03
S56	2	"712".clas. AND (microcontroller SAME address SAME relative SAME absolute SAME stack)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/15 13:46
S57	0	S49.ti. AND instruction NEAR2 (partition\$3 OR split \$3) NEAR10 control	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/15 13:47
S58	3	S49.ti. AND instruction NEAR2 (partition\$3 OR split \$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/15 13:48
S59	108	S49 AND instruction NEAR2 (partition\$3 OR split\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/15 13:48
S60	44919	(reconfigurable hardware OR reconfigurable logic OR programmable logic array OR programmable logic devices OR Field Programmable Gate Array OR Programmable Array Logic OR Logic Cell Arrays)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/15 16:23

S61	101	S60 NEAR5 (mux OR multiplexor OR multiplexer) NEAR5 output	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/15 16:28
S62	150	S60 NEAR5 (mux OR multiplexor OR multiplexer) NEAR5 (output OR input)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/15 16:28
S63	286898	(reconfigurable hardware OR reconfigurable logic OR programmable logic array OR PLA OR programmable logic devices OR PLD OR Field Programmable Gate Array OR FPGA OR Programmable Array Logic OR PAL OR EPLD EEPLD OR Logic Cell Arrays OR LCA)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/18 10:00
S64	66	"712".clas. AND S63.ti.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/18 10:01
S65	4	"6721884"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/18 10:35
S66	19	"6023755"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/18 10:55
S67	14	"6145072"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/18 10:51
S68	8	"6172521"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/18 10:44

S69	3530	motomura.in.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/18 10:45
S70	50	motomura.in. AND (reconfigur \$7 OR \$2programmable)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/18 11:16
S71	100	"5684950"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/18 10:55
S72	77	"5684980"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/18 10:55
S73	173	"712".clas. AND (reconfigur \$6 comput\$3 OR RFPGAS OR programmable NEAR3 on the fly OR dynamic comput\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/18 11:19
S74	166	"712".clas. AND (reconfigur \$6 comput\$3 OR RFPGAS OR programmable NEAR3 on the fly OR dynamic comput\$3) NOT S64	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 08:08
S75	153	"712".clas. AND dynamically \$2configurable NOT S73	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/18 11:35
S76	79	"712".clas. AND dynamically \$2configurable AND S63 NOT S73	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/18 11:40
S77	0	326/38.clas.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/18 11:40

S78	1553	326/38.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 08:03
S79	2777	326/38.ccls. AND control flow OR instruction flow	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/18 11:40
S80	16	326/38.ccls. AND (control flow OR instruction flow)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/18 11:40
S81	286966	(reconfigurable hardware OR reconfigurable logic OR programmable logic array OR PLA OR programmable logic devices OR PLD OR Field Programmable Gate Array OR FPGA OR Programmable Array Logic OR PAL OR EPLD EEPLD OR Logic Cell Arrays OR LCA)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/08/09 09:46
S82	66	"712".clas. AND S81.ti.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 08:03
S83	55	326/38.ccls. AND (reconfigur \$6 comput\$3 OR RFPGAS OR programmable NEAR3 on the fly OR dynamic comput\$3 OR dynamically configurable) NOT S82	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 08:07
S84	44972	(reconfigurable hardware OR reconfigurable logic OR programmable logic array OR programmable logic devices OR Field Programmable Gate Array OR Programmable Array Logic OR Logic Cell Arrays)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 08:09
S85	735	"712".clas. AND S84 AND (control SAME (memory or command or code OR device) SAME address)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 08:10

S86	173	"712".clas. AND (reconfigur \$6 comput\$3 OR RFPGAS OR programmable NEAR3 on the fly OR dynamic comput\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 08:12
S87	3	S86 AND cache AND address translation	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 08:11
S88	90	"712".clas. AND (reconfigur \$6 hardware) AND (partition \$3 OR split\$3 OR separat\$3 OR divid\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 08:14
S89	13	"712".clas. AND (self reconfiguration OR self re \$1configuration OR selfconfiguration OR self configuration OR self \$1configuration)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 08:20
S90	121	abramovici.in.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 08:20
S91	14	("4541071" "5452231" "5574388" "5604888" "5644515" "5831996" "5903744" "6009256" "6292916").PN. OR ("6442732").URPN.	US-PGPUB; USPAT; USOCR	ADJ	ON	2006/12/19 08:24
S92	42	(plurality NEAR3 S84) SAME (\$5controller OR \$5control) SAME (instruction OR command)	US-PGPUB; USPAT; USOCR	ADJ	ON	2006/12/19 08:26
S93	29	("712".clas. OR 326/38.ccls.) AND (runtime reconfiguration OR run-time reconfiguration)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 09:42
S94	36	"712".clas. AND (reconfigurable comput\$3) AND (instruction sequence OR instruction flow OR control flow OR control sequence OR microoperations OR microops OR micro-ops)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 09:43

S95	2	"712".clas. AND opcode NEAR3 (flag OR bit) NEAR3 (following or next OR present) NEAR3 (execut\$3 OR stop\$3 OR ended)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 10:46
S96	0	"712".clas. AND opcode NEAR3 (flag OR bit) NEAR3 (break)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 10:46
S97	1	"712".clas. AND opcode NEAR3 (flag OR bit OR parameter) NEAR3 (stop\$3 OR halt\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 10:47
S98	86	"712".clas. AND (flag OR bit OR parameter) NEAR3 (following OR next OR subsequent) (instruction OR command) NEAR3 (execut\$3 OR run\$3 OR ignor\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 10:53
S99	0	"712".clas. AND thread NEAR3 (stop\$3 OR terminat \$3 OR end\$3) bit	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 10:57
S100	43	"712".clas. AND nop NEAR3 bit NEAR3 instruction	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 11:01
S101	64	"712".clas. AND (stop OR halt) NEAR3 (parameter OR bit) NEAR3 instruction	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 11:03
S102	361	"712".clas. AND (stop OR halt) NEAR3 (parameter OR bit)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 11:04
S103	216	"712".clas. AND (stop OR halt) (parameter OR bit)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 11:08

S104	90	"712".clas. AND (stop OR halt) (parameter OR bit) SAME instruction	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 13:38
S105	44972	(reconfigurable hardware OR reconfigurable logic OR programmable logic array OR programmable logic devices OR Field Programmable Gate Array OR Programmable Array Logic OR Logic Cell Arrays)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 13:39
S106	89	S105 AND (cache control\$3) AND (address translation)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 13:40
S107	0	S105.ti. AND (cache control \$3) AND (address translation)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 13:42
S108	7	S105.ti. AND (cache control \$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 13:56
S109	0	S105.ti. AND (cache control \$3) AND translation	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 13:43
S110	0	S105.ti. AND (cache control \$3) AND TLB	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 13:43
S111	0	S105.ti. AND (cache control \$3) AND virtual	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 13:56
S112	126	(cache controller SAME address translat\$3 SAME cache)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 14:00

S113	0	S105.ti. AND MMU AND translat\$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/19 14:00
S114	25	("6085317").URPN.	USPAT	ADJ	ON	2006/12/19 16:54
S115	1	2001-331767	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/20 10:48
S116	1	2001-147802	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/20 10:48
S117	11	"3099889"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/20 10:49
S118	0	200-181566	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/20 10:49
S119	3	2000-181566	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/20 10:49
S120	1	2000-040745	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/20 10:49
S121	0	2000-040745 A	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/20 10:49

S122	0	2000-040745A	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/20 10:49
S123	1	11-184718	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/12/20 10:49
S124	78	(reconfigurable hardware OR reconfigurable logic OR programmable logic array OR PLA OR programmable logic devices OR PLD OR Field Programmable Gate Array OR FPGA OR Programmable Array Logic OR PAL OR EPLD OR EEPLD OR Logic Cell Arrays OR LCA) SAME divi\$4 SAME program SAME (processing unit OR logic block Or logic unit)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/08/09 09:56
S125	71	(program NEAR7 (divid\$4 OR partition\$3)) NEAR10 reconfigur\$5	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/08/09 10:23
S126	56	"3812469"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/08/09 10:02
S127	10	"6658564"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/08/09 10:27
S128	3	"6637017"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/08/09 10:28
S129	9	"6633181"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/08/09 10:29

S130	14	"6438737"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/08/09 10:32
S131	15	"6077315"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/08/09 10:40
S132	2	"20030056091"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/08/09 11:15
S133	10	"6658564"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/08/09 11:15
S134	2	"20030056041"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/08/09 14:13
S135	2	"20030056091"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/08/09 14:14
S136	14	"6438737"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/08/09 14:15
S137	15	"6077315"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/08/09 14:15
S138	23	"712".clas. AND \$2configura \$5 NEAR4 (memory OR bank) NEAR4 (multiplexer OR multiplexor OR mux OR selector)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/28 17:28

S139	275	"712".clas. AND (bank) NEAR4 (multiplexer OR multiplexor OR mux OR selector)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/28 15:29
S140	5	"712".clas. AND (bank) NEAR4 (multiplexer OR multiplexor OR mux OR selector) NEAR10 (speed OR quick OR fast OR performance)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/28 15:25
S141	1	"712".clas. AND (bank) NEAR4 (multiplexer OR multiplexor OR mux OR selector) NEAR7 (prefetch OR preload)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/28 15:30
S142	223	"712".clas. AND (multiplexer OR multiplexor OR mux OR selector) NEAR7 (prefetch OR preload)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/28 15:31
S143	0	"712".clas. AND (multiplexer OR multiplexor OR mux OR selector) NEAR7 (prefetch OR preload) NEAR10 (configuration OR reconfiguration)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/28 15:32
S144	96	"712".clas. AND (prefetch OR preload) NEAR10 (configuration OR reconfiguration)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/28 15:32
S145	17	"712".clas. AND (prefetch OR preload) NEAR10 (configuration OR reconfiguration) AND (FPGA OR PLD OR PLA OR PAL)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/28 15:51
S146	58	"712".clas. AND (bank OR port) NEAR10 select\$3 NEAR10 (configuration OR reconfiguration)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/28 17:24
S147	6	"712".clas. AND memory (bank OR port) NEAR10 select \$3 NEAR10 (configuration OR reconfiguration)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/28 17:26

S148	94	(configuration memory bank)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/28 17:27
S149	3	(configuration memory bank) AND (fpga OR PLA OR PAL OR PLD)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/28 17:27
S150	655	\$2configura\$5 NEAR4 (memory OR bank) NEAR4 (multiplexer OR multiplexor OR mux OR selector) NOT S138	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/28 17:29
S151	505	\$2configura\$5 NEAR4 (memory OR bank) NEAR4 (multiplexer OR multiplexor OR mux) NOT S138	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/28 17:29
S152	355	\$2configura\$5 NEAR4 (memory OR bank) NEAR4 (multiplexer OR multiplexor OR mux) NOT S138 AND (FPGA OR PLD OR PAL OR PLA OR programmable logic array OR programmable logic device OR field programmable gate array)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/28 17:30
S153	24	"6288566"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/28 17:30
S154	78	"5970254"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/29 08:06
S155	24	"6288566"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/29 08:07

S156	25	"6349346"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/29 08:11
S157	27	("5426378" "5550782" "5760602" "5796269" "6005865" "6018559" "6091263").PN. OR ("6288566").URPN.	US-PGPUB; USPAT; USOCR	ADJ	ON	2008/01/29 08:45
S158	0	cancel\$prg	US-PGPUB; USPAT; USOCR	ADJ	ON	2008/01/29 08:46
S159	2	cancel\$4 NEAR5 load\$3 NEAR5 command NEAR7 memory	US-PGPUB; USPAT; USOCR	ADJ	ON	2008/01/29 08:47
S160	80	cancel\$4 NEAR5 load\$3 NEAR7 memory	US-PGPUB; USPAT; USOCR	ADJ	ON	2008/01/29 08:49
S161	14	configura\$4 NEAR5 memory NEAR10 cancel\$4	US-PGPUB; USPAT; USOCR	ADJ	ON	2008/01/29 08:51
S162	58	configura\$4 NEAR15 memory NEAR10 cancel\$4	US-PGPUB; USPAT; USOCR	ADJ	ON	2008/01/29 08:56
S163	141	cancel\$4 NEAR2 load NEAR3 (operation OR instruction)	US-PGPUB; USPAT; USOCR	ADJ	ON	2008/01/29 08:58
S164	32	cancel\$4 NEAR2 configuration NEAR3 (operation OR instruction)	US-PGPUB; USPAT; USOCR	ADJ	ON	2008/01/29 08:57
S165	2	(cancel\$4 NEAR2 load NEAR3 (operation OR instruction)) AND (FPGA OR PLA OR PAL OR PLD OR programmable gate array OR programmable logic array or programmable logic device OR CLB OR configurable logic block)	US-PGPUB; USPAT; USOCR	ADJ	ON	2008/01/29 09:21
S166	2	(cancel\$4 NEAR2 (prefetch\$3 OR preload\$3)) AND (FPGA OR PLA OR PAL OR PLD OR programmable gate array OR programmable logic array or programmable logic device OR CLB OR configurable logic block)	US-PGPUB; USPAT; USOCR	ADJ	ON	2008/01/29 09:21
S167	8	(cancel\$4 NEAR10 (prefetch \$3 OR preload\$3)) AND (FPGA OR PLA OR PAL OR PLD OR programmable gate array OR programmable logic array or programmable logic device OR CLB OR configurable logic block)	US-PGPUB; USPAT; USOCR	ADJ	ON	2008/01/29 09:45
S168	1	"20020144054"	US-PGPUB; USPAT; USOCR	ADJ	ON	2008/01/29 09:28

S169	46	cancel\$4 NEAR5 (prefetch\$3 OR preload\$3) NEAR5 (instruction OR operation)	US-PGPUB; USPAT; USOCR	ADJ	ON	2008/01/29 09:29
S170	59	cancel\$4 NEAR5 (prefetch\$3 OR preload\$3) NEAR5 (instruction OR operation)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/29 09:37
S171	1	JP362203236A	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/29 09:37
S172	0	"362203236"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/29 09:37
S173	1	"62203236"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/29 09:44
S174	344	configuration NEAR6 halt	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/29 09:45
S175	145	(configuration NEAR10 halt \$3) AND (FPGA OR PLA OR PAL OR PLD OR programmable gate array OR programmable logic array or programmable logic device OR CLB OR configurable logic block)	US-PGPUB; USPAT; USOCR	ADJ	ON	2008/01/29 09:47
S176	63	(configuration NEAR10 halt\$3 NEAR3 (instruction OR operation Or command)) AND (FPGA OR PLA OR PAL OR PLD OR programmable gate array OR programmable logic array or programmable logic device OR CLB OR configurable logic block)	US-PGPUB; USPAT; USOCR	ADJ	ON	2008/01/29 09:47
S177	5	(configuration memory) NEAR25 (control flow)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/29 11:07

S178	117	(configuration memory) NEAR25 (configur\$5 NEAR2 instruction)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/29 12:40
S179	77	"5915123"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/29 11:18
S180	64	(configuration memory) NEAR10 (load\$3 NEAR3 instruction)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/29 13:03
S181	17	("5068823" "5142625" "5535342" "5548228" "5684980" "5966534" "5968161" "6085317" "6282627").PN. OR ("6658564").URPN.	US-PGPUB; USPAT; USOCR	ADJ	ON	2008/01/29 13:38
S182	4793	(configuration controller)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/29 17:08
S183	82	(configuration controller) NEAR10 (configuration data)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/29 17:11
S184	320	(configuration controller) AND (configurable logic block OR clb OR fpga OR pld OR PAL OR PLA)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/29 17:12
S185	34	(configuration controller) NEAR10(configurable logic block OR clb OR fpga OR pld OR PAL OR PLA)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/29 17:25
S186	17	(reconfiguration controller) NEAR10(configurable logic block OR clb OR fpga OR pld OR PAL OR PLA)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/29 17:28

S187	0	(reconfiguration controller) NEAR15 (state machine)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/29 17:28
S188	39	(configuration controller) NEAR15 (state machine)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/29 17:36
S189	96	(online OR on-line) reconfiguration	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/29 17:36
S190	13	(online OR on-line) reconfiguration AND fpga	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/29 17:36
S191	255	DPGA	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/29 17:38
S192	98	(dynamic\$4 programmable gate array)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/29 17:46
S193	78	"5970254"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/29 17:46
S194	28	("5684980" "5802290" "5838167" "6094063" "Re34363").PN. OR ("6326806").URPN.	US-PGPUB; USPAT; USOCR	ADJ	ON	2008/01/29 17:58
S195	17	"6077315"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/29 18:03

S196	10	("5336950" "5784636" "5995744" "6038400" "6157210").PN. OR ("6260139").URPN.	US-PGPUB; USPAT; USOCR	ADJ	ON	2008/01/29 18:11
S197	69	"4860192"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/30 08:23
S198	0	specialized direct output	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/30 08:24
S199	0	(configuration controller) NEAR15 (state machine) AND @pd> = "20080129"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/02 17:19
S200	5	(configuration controller) NEAR10 (configurable logic block OR clb OR fpga OR pld OR PAL OR PLA) AND @pd> = "20080129"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/02 17:19
S201	13	(configuration controller) NEAR10 (configuration data) AND @pd> = "20080129"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/02 17:20
S202	14	"712".clas. AND (reconfigur \$6 hardware) AND (partition \$3 OR split\$3 OR separat\$3 OR divid\$3) AND @pd> = "20080129"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/02 17:22
S203	3	(program NEAR7 (divid\$4 OR partition\$3)) NEAR10 reconfigur\$5 AND @pd> = "20080129"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/02 17:23
S204	13	(online OR on-line) reconfiguration AND @pd> = "20080129"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/02 17:24

S205	1	cancel\$4 NEAR5 (prefetch\$3 OR preload\$3) NEAR5 (instruction OR operation) AND @pd>="20080129"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/02 17:24
S206	1	"712".clas. AND (prefetch OR preload) NEAR10 (configuration OR reconfiguration) AND @pd>="20080129"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/02 17:24
S207	68035	(reconfigurable hardware OR reconfigurable logic OR programmable logic array OR programmable logic devices OR Field Programmable Gate Array OR Programmable Array Logic OR Logic Cell Arrays)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/02 17:25
S208	0	S207.ti. AND control flow analysis AND @pd>="20080129"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/02 17:25
S209	18	"712".clas. AND (reconfigur \$6 comput\$3 OR RFP GAS OR programmable NEAR3 on the fly OR dynamic comput\$3) AND @pd>="20080129"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/02 17:26
S210	1973	326/38.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/02 17:27
S211	537	712/220.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/02 17:29
S212	0	inter-configuration communication	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/02 17:48
S213	102	inter NEAR2 \$2configuration NEAR2 communication	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/02 17:48

S214	57	(multiple context OR multi context OR multi-context) NEAR5 (FPGA OR PLD OR PAL OR PLA OR reconfigura\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/02 17:51
S215	11	(runtime NEAR2 reconfiguration) AND @pd>="20080129"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/02 17:57
S216	0	(runtime NEAR2 reconfiguration) NEAR5 (sequenc\$3 OR schedul\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/02 17:58
S217	920	(reconfigura\$4) NEAR5 (sequenc\$3 OR schedul\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/02 17:58
S218	453	"712".clas. AND (\$2configura\$4) NEAR5 (sequenc\$3 OR schedul\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/02 17:59
S219	128	"712".clas. AND (\$2configura\$4) NEAR5 (sequenc\$3 OR schedul\$3) NEAR10 control\$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/02 18:00
S220	27	"6105105"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/02 18:18
S221	31	("4812989" "5101483" "5105421" "5129079" "5333287" "5412785" "5684980").PN. OR ("6105105").URPN.	US-PGPUB; USPAT; USOCR	ADJ	ON	2008/09/02 18:18
S222	14	"6571381"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/03 07:45

S223	2	(configuration controller) NEAR15 (state machine) AND @pd> = "20080092"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 16:26
S224	15	(configuration controller) NEAR10 (configurable logic block OR clb OR fpga OR pld OR PAL OR PLA) AND @pd> = "20080092"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 16:27
S225	35	(configuration controller) NEAR10 (configuration data) AND @pd> = "20080092"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 16:27
S226	35	(configuration controller) NEAR10 (configuration data) AND @pd> = "20080092"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 16:28
S227	39	"712".clas. AND (reconfigur \$6 hardware) AND (partition \$3 OR split\$3 OR separat\$3 OR divid\$3) AND @pd> = "20080092"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 16:29
S228	10	(program NEAR7 (divid\$4 OR partition\$3)) NEAR10 reconfigur\$5 AND @pd> = "20080092"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 16:30
S229	32	(online OR on-line) reconfiguration AND @pd> = "20080092"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 16:31
S230	6	cancel\$4 NEAR5 (prefetch\$3 OR preload\$3) NEAR5 (instruction OR operation) AND @pd> = "20080092"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 16:31
S231	12	"712".clas. AND (prefetch OR preload) NEAR10 (configuration OR reconfiguration) AND @pd> = "20080092"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 16:32

S232	49	"712".clas. AND (reconfigur\$6 comput\$3 OR RFPGAS OR programmable NEAR3 on the fly OR dynamic comput\$3) AND @pd>="20080092"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 16:32
S233	329	326/38.ccls. AND @pd>="20080092"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 16:35
S234	6	((("4812989" "5101483" "5105421" "5129079" "5333287" "5412785" "5684980").PN. OR ("6105105").URPN.) AND @pd>="20080092"	US-PGPUB; USPAT; USOCR	ADJ	ON	2009/04/22 16:36
S235	3	(reconfigur\$4 hardware).ti. AND (command OR instruction) near2 (control OR manager) AND @pd>="20080092"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 16:36
S236	6	(reconfigur\$4 hardware).ti. AND (command OR instruction) near2 (control OR manager) AND @pd>="20061220"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 16:37
S237	0	(reconfigur\$4 hardware) AND control (device OR manager) NEAR10(stop OR halt OR activate OR load OR cancel OR wait) NEAR2 instruction AND @pd>="20061220"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 16:37
S238	342475	(reconfigurable hardware OR reconfigurable logic OR programmable logic array OR PLA OR programmable logic devices OR PLD OR Field Programmable Gate Array OR FPGA OR Programmable Array Logic OR PAL OR EPLD EEPLD OR Logic Cell Arrays OR LCA)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 16:38
S239	6	S238 AND (control device SAME (switch\$3)) SAME (circuit model OR netlist OR page OR circuit portion OR tiles OR blocks) SAME (activate OR halt OR interrupt OR load OR cancel OR wait) AND @pd>="20061220"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 16:38

S240	101	configuration NEAR6 halt AND @pd>="20061220"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 16:39
S241	25	(configuration memory) NEAR25 (configur\$5 NEAR2 instruction) AND @pd>="20061220"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 16:39
S242	23	(configuration controller) NEAR10(configurable logic block OR clb OR fpga OR pld OR PAL OR PLA) AND @pd>="20061220"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 16:40
S243	0	(divid\$3 NEAR6 application program)".clm"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 16:41
S244	29	(divid\$3 NEAR6 application program).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 16:41
S245	13	(command code sequence\$3). clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 16:42
S246	3	(program NEAR5 creat\$3 NEAR5 logic circuit).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 16:44
S247	2	(effective block select\$3). clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 16:45
S248	5439	(switch\$3 NEAR5 program). clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 16:45

S249	1	(switch\$3 NEAR5 program NEAR10 reconfigurable).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 16:45
S250	4	(control flow NEAR5 application program).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 16:46
S251	1088	(completion data).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 16:46
S252	17	(completion data NEAR5 program).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 16:46
S253	60	(structural information NEAR5 computer).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 16:47
S254	695887	(command sets NEAR5 computer OR input).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 16:48
S255	179	(command sets NEAR5 (computer OR input)).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 16:48
S256	32	(control flow analysis).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 16:48
S257	2	(command sequence intermediate).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 16:49

S258	201	(activate command).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 16:50
S259	1	(activate command NEAR20 \$2configura\$4).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 17:21
S260	0	(activate instruction NEAR20 \$2configura\$4).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 17:21
S261	17	(activate instruction).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 17:21
S262	24548	(activat\$3 NEAR10 process \$3).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 17:21
S263	46	(activat\$3 NEAR10 processing element).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 17:22
S264	97	(halt command).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 17:22
S265	103	(halt instruction).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 17:25
S266	0	(load\$prg).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 17:26

S267	291	(cancel (operation OR command)).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 17:27
S268	3	(cancel (operation OR command) NEAR20 \$2configura\$5).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 17:27
S269	1	(wait (operation OR command) NEAR20 \$2configura\$5).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 17:28
S270	1	(wait\$3 (operation OR command) NEAR20 \$2configura\$5).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 17:28
S271	239	(wait\$3 (operation OR command)).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 17:28
S272	13	(command code memory).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 17:30
S273	1	(command code reference device).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 17:30
S274	111	712/220.ccls. AND @pd>="20080902"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/04/22 17:32

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